

IMS ENGINEERING COLLEGE, GHAZIABAD

Department of Electronics & Communication Engineering

Dated: 11.06.2014

Faculty Development Program by ECE Department

A three day faculty development program (FDP) on “VLSI Technologies & Advanced Embedded Systems” by Tevatron Technologies, Noida in association with ECE Department, IMSEC is going to held at ‘A’ block conference hall, IMS Engineering College, Ghaziabad from 11th June’ 2014 to 13th June’ 2014. The prospective participants of this three day FDP would be the faculty members of ECE and EN Departments of IMS Engineering College, Ghaziabad.

The resource persons would be Er. Rajeev Tiwari and Er. Anurag Gupta, Co-founders Tevatron Technologies (P) Ltd., Noida. Also there will be expert talk by Er. Ishtyaq Malik, who is the Associate Design Engineer at Tevatron Technologies.



The inaugural function of this FDP program held at Block-A conference hall, IMSEC on 11th June’ 2014 at 9:30AM, with Dr. S. N. Rajan, Dean (R&D) of IMSEC, presided over the entire event, along with Deans, HoDs and participating faculty members.

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Program Details: Three day faculty development program (FDP) on VLSI Technologies & Embedded Systems by Tevatron Technologies, Noida in association with ECE Department, IMSEC.

Duration: 11th – 13th June' 2014

Venue: IMSEC, Ghaziabad

Schedule of Inaugural Function at 'A' Block Conference Hall, IMSEC, Ghaziabad

Time	Program Details
9:30AM - 9:45AM	Arrival of Guests (Mr. Rajeev Tiwari, Mr. Anurag Gupta from Tevatron Tech. & Dr. S. P. Pandey, Director, IMSEC)
9:45AM - 9:55AM	Welcome Address
9:55AM - 10:00AM	Felicitation of guests by HoD, ECE
10:00AM - 10:10AM	Formal inauguration of FDP program by Director Sir
10:10AM - 10:25AM	Talk by Mr. Rajeev Tiwari
10:25AM - 10:40AM	Talk by Mr. Anurag Gupta
10:40AM - 10:45AM	Vote of Thanks