(Following Paper ID and Roll No. to be filled in your Answer Book)

## PAPERID. 0321

 Roll No.
B. Tech
(SEM III) ODD SEMESTER THEORY EXAMINATION 2009-10 ANALOG \& DIGITAL ELECTRONICS

Time: 3 Hours]
[Total Marks : 100
Note : Attempt all questions. All questions carry equal marks.

1 Attempt any four parts of the following :
(a) Draw the V-I characteristics of tunnel diode and indicate the useful region in the curve.
(b) Draw the output characteristics of transistor. How it is used as a switch ?
(c) Describe the characteristics and application of photodiode.
(d) An LED is connected across a voltage source of +10 V through a series resistance of $8 \mathbf{2} 0 \Omega$. Calculate the LED current. Assume the voltage drop across an LED of 15 Volt.
(e) Explain the working of tunnel diode. Give its two applications.
(f) Why photodiode is used in reverse bias conditions? Give any two applications of it.

2 Attempt any four parts of the following :
(a) An amplifier has a midband gain of 1500 and a bandwidth of 4.0 MHz , the midband gain reduces to 150 when a negative feedback is applied. Determine the value of feedback factor and Bandwidth.
(b) Draw the high frequency equivalent circuit for the typical RC coupled common emitter amplifier.
(c) Explain the effect of negative feedback on various characteristics of the amplifier.
(d) An RC coupled amplifier has a voltage gain of $1000, f_{1}=50 \mathrm{~Hz}, \quad f_{2}=200 \mathrm{kHz}$ and a distortion of $5 \%$ without feedback. Find the amplifier voltage gain, $f_{1}^{\prime}, f_{2}^{\prime}$ and distortion when negative feedback is applied with feedback ratio of 0.01 .
(e) Calculate the voltage gain, input and output resistances of a voltage series feedback amplifier having

$$
A_{v}=300, R_{i}=1.5 \mathrm{k} \Omega, R_{o}=50 \mathrm{k} \Omega \text { and }
$$

$$
\beta=1 / 15
$$

(f) Describe the properties of series-shunt and shuntshunt feedback amplifier.

3 Attempt any two parts of the following :
(a) What is Barkhausen criterion for the Feedback oscillator ? Draw a neat diagram of a phase-shift oscillator using BJT. Derive an expression for its frequency of oscillation.
(b) Explain the working of Wein-bridge oscillator. Derive formula for the frequency of oscillation.
(c) A Colpitt's oscillator is designed with $\mathrm{C}_{1}=100 \mathrm{pf}$ and $C_{2}=7500 \mathrm{pf}$. The inductance is variable.

Determine the range of inductance values, if the frequency of oscillation is to vary between 950 kHz and 2050 kHz ?

4 Attempt any two parts of the following :
(a) What do you mean by flip-flops? Describe the edge triggered flip-flops. Convert SR flip-flops into JK flip-flops.
(b) Implement the boolean function using 8:1 multiplexer.

$$
F(A, B, C, D)=\bar{A} B \bar{D}+A C D+\bar{B} C D+\bar{A} \bar{C} D
$$

Describe the working of counter.
(c) Draw and explain the working of 4-bit up and down synchronous counter. Also describe the working of shift register.

5 Attempt any two parts of the following :
(a) Describe the working of 555 timer. How it works in A-stable operation?
(b) Explain $\mathrm{A} / \mathrm{D}$ converter using voltage to frequency converter. Describe any one method of $A / D$ converter.
(c) Explain the organisation of RAM with the help of neat diagram. Also describe the switchin regulators.

