Drinted reason	01		Sub Code: NEE504
Printed pages:	01	Roll No:	
Paper Id:	2 0 3 4	Lasers annaures after stress surgers adaptions, a basing other stress of	
B TECH			
(SEM V) THEORY EXAMINATION 2017-18 MICROPROCESSOR & ITS APPLICATIONS			
	MICROPROCESSOR & I	TS APPLICATIONS	Total Marks: 100
Time: 3 Hours	Ş		Total Warks. 100
Notes: Attempt all Sections. Assume any missing data.			
1101001120001	SECTION	-A	
1 Attama			(2x10=20)
1. Attemp	ot all question in brief: Write about the basic difference between	microprocessor and microcontro	oller.
a)	What are interfacing logical devices?	initer option of the second seco	
D)	Define following: (i)Nibble(ii)word		
c)	Define following: (i) Mnemonics(ii)Progr	am	
a)	Write basic operations of microprocessor	with block diagram.	
	Write about different languages of digital	computer.	
f)	Define compiler or interpreter in program	ming languages.	
g)		185.	
	Draw flag register of 8085.		
i)	Write about types of addressing modes in	8086	
j)	while about types of addressing modes in	ION -B	
			(10x3=30)
2. Attemp	pt any three parts of the following: in Minimum Mode operation of 8086 micro	oprocessor with block diagram.	
a) Explai	are Procedure & Macros in assembler direct	ctives of 8086.	
b) Comp	in the following instructions of 8085 micro	processors	
c) Explan	in the following histractions of 6000 milere	provessors	
	POP PSW		
b)	OT YYT		
c)			
	PUSH PSW		
e)) CMP M the features and functional block diagram of	of 8237 DMA controller.	
d) Given	in the internal architecture of 8255		
e) Expla	in the internal architecture of 8255. SECTI	ON -C	
			(10x1=10)
3. Attempt an	y one parts of the following:	its different generation. What	t do you mean by
 3. Attempt any one parts of the following: a) Explain evolution of microprocessor with its different generation. What do you mean by Addressing mode, explain Different addressing mode used in 8085 with suitable example. 			
b) Draw architecture of 8086 explain its different unit. What do you mean by pipelining and			
b) [)raw architecture of 8086 explain its unit	nome unit. What do you ment	J 1 1 -
e	explain the concept of memory segmentation	11.	(10x1=10)
4. Attempt an	y one parts of the following:	the flowchart of assembler leve	1 programming?
a) Expla	ain assembler level programming and draw	the now chart of assembler is the	FF-0
b) Expla	ain following:		
(i) 82	259 Programmable interrupt controller.	Linkor Emulator	
(ii) D	Development tools: Editor, Library builder,	Lilikei, Elinulator.	(10x1=10)
5. Attempt an	ny one parts of the following:	0070	(10,000)
1	1 I'rement MODES OF OPERALIUN OT	8239.	
b) Expla	ain minimum and maximum operating mod	les of 8080 with thining diagram	(10x1=10)
6 Attomat at	ny one parts of the following:		(I VIAL AU)
a) Drow	and evolain block diagram and Din CONILS	uration of IC-8255.	
b) Write	e an assembly level program to find square	root of given number	(10x1=10)
7 Attament or	ny one parts of the following:		(TAT TA)
a) Expl	ain the interrupts sequence and types of int	errupt in 8086.	
b) Drav	w explains the memory and I/O read cycle of	of 8085.	
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