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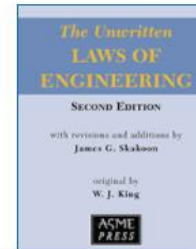
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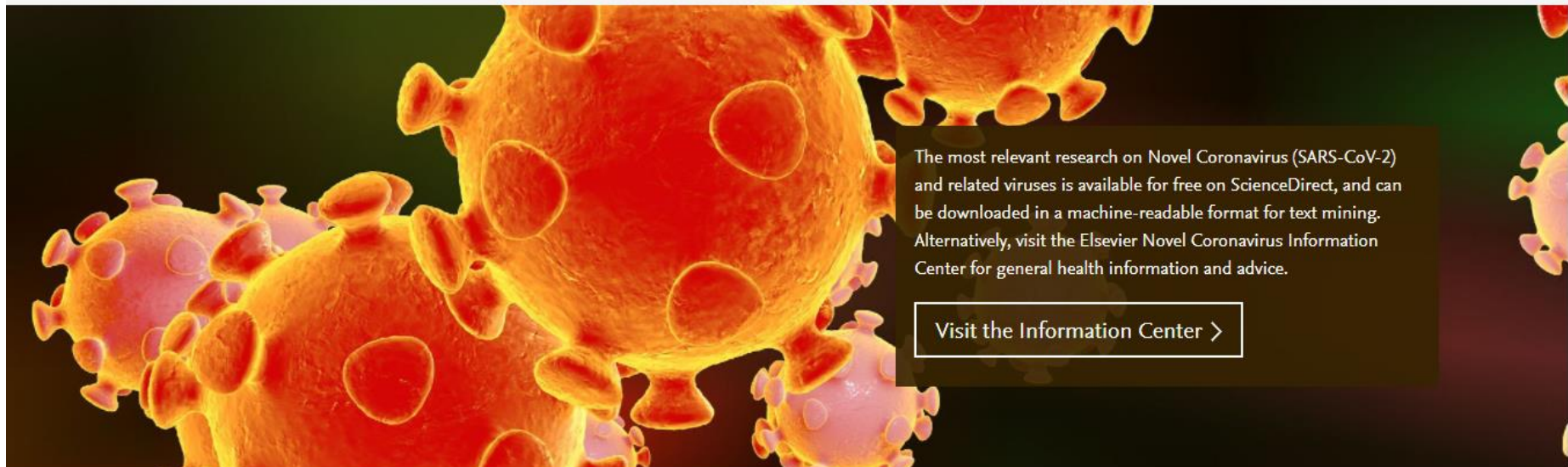
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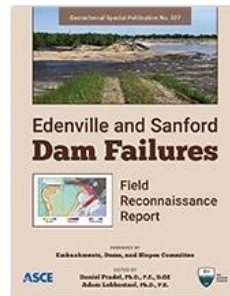
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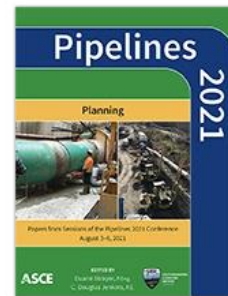
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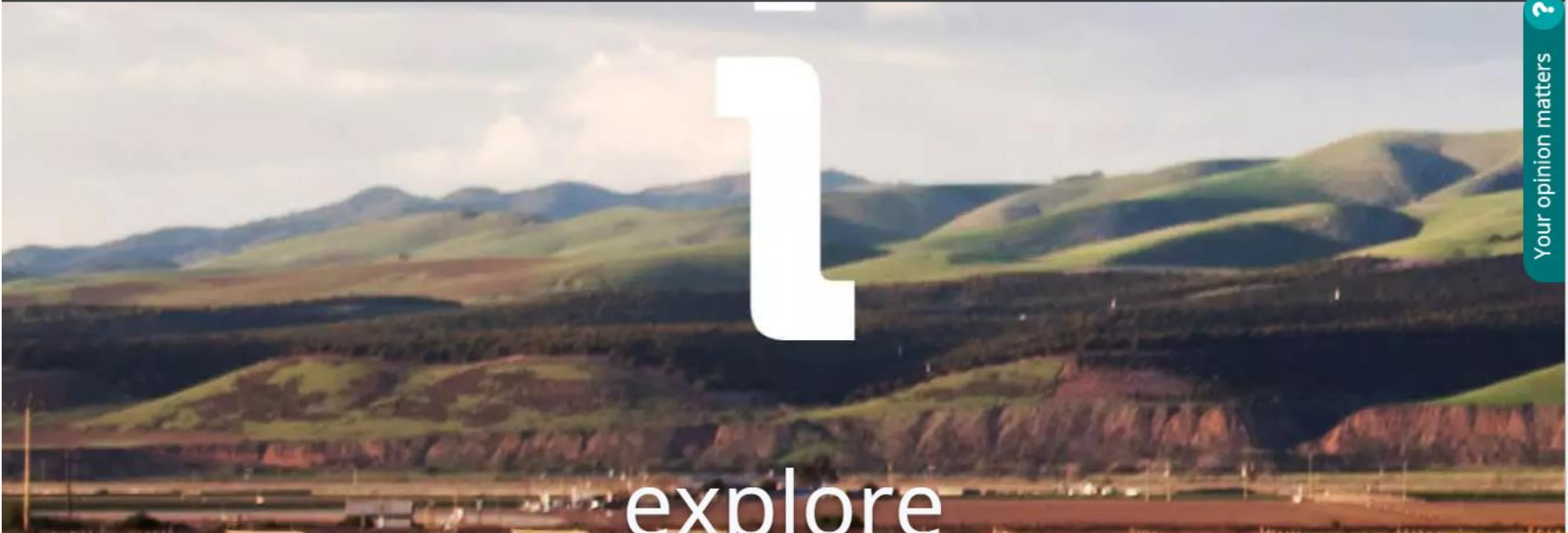
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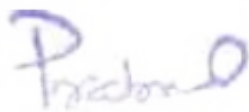
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On this Day

Person of the Week: Blaise Pascal

Topic of the Week: IIT Kharagpur Established

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Analysis and Synthesis of Logic Functions using Decoders



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Introduction

About the Experiment

This experiment enables a student to learn

- How to realize functionality of a 3-to-8 line active low Decoder viz. 74138 IC. That is on setting the two active low and one active high enable inputs to proper level, one can verify that one and only one of the eight active low outputs is asserted based on the values assigned to three select input.
- How to cascade two 74138 IC's to implement a 4-to-6 active low decoder.